



# General Micro Systems (GMS) Introduces X9 SPIDER 3U OpenVPX Single Board Systems with 455Gbps I/O Throughput, SOSA™ Alignment

Two new scalable, modular Intel<sup>®</sup>-based OpenVPX "single board systems," developed with the Modular Open Standards Approach (MOSA) and SOSA<sup>™</sup>-aligned, offer more I/O, processing, and patented cooling technology in 3U than is possible in 6U size

Washington D.C., October 11, 2021–General Micro Systems today launched two MOSAinspired 3U OpenVPX "single board systems" with more processing, storage and I/O than is available in 6U-sized boards twice the size. Based on an Intel<sup>®</sup> 11<sup>th</sup> gen Tiger Lake-H 8 core Core i7 CPU with 64GB of memory, X9 SPIDER VPX boards are available in conduction- and air-cooled versions. Both products are intended for deployed military and aerospace or rugged applications and were designed in alignment with The Open Group Sensor Open Systems Architecture (SOSA<sup>™</sup>) technical standard.

Unique to these modules is a massive I/O bandwidth via multiple sealed USB4 and 100Gbit Ethernet ports—up to 455Gbps—that frees designers from the bottlenecks of the OpenVPX backplane, yet they remain interoperable with other industry vendors' boards and systems. Also available are up to four on-board SSDs for embedded RAID data storage, two PCIe-Mini sites, plus an add-in NVIDIA<sup>®</sup> RTX5000GPGPU artificial intelligence (AI) co-processor and a 12VDC-only scalable architecture that transcends the restrictive OpenVPX bus. One GMS module set literally replaces an entire chassis worth of OpenVPX modules.

"The upside of OpenVPX and SOSA is interoperability and the spirit of the DoD's push towards a modular open standards approach (MOSA), but the backplane can also be restrictive for I/O, bandwidth and upgradeability," says Ben Sharfi, GMS founder and chief architect. "Our X9 SPIDER VPX-HS and VPX-S modules provide 455Gbps of beyond-thebackplane I/O, patented cooling, sealed USB4/Thunderbolt4<sup>™</sup> and scalability. Even though they're SOSA aligned and interoperable, we also provide the industry's highest-performance features and a path for system-level technology refresh with CPU and pinout upgrades that avoid backplane re-spins."

# Really Cool: Two "Single Board Systems"

Based upon Intel's Tiger Lake-H 8 core CPU, X9 SPIDER VPX-HS is a two-slot "single board system" equipped with sealed, quad 40Gbps USB4 Type-C ports and dual 100Gbps Ethernet ports, which collectively provide 360Gbps of data pipelines to other boards, systems and sensors. The -HS version also includes two additional M.2 SSD sites and a high bandwidth NVIDIA GPGPU co-processor MXM site that doubles as a PCIe bus extender. The companion single-slot X9 SPIDER VPX-S is also a "single board system" that omits the dual 100Gbps Ethernet fiber ports, the MXM site, and has dual M.2 SSDs. Both VPX module types also include two PCIe-Mini add-in card sites, 10Gb Ethernet, USB 3.2 and other on-board ports, boasting up to 455Gbps of total system throughput (-HS).

The conduction-cooled "single board system" modules are so densely packed that they can replace an entire chassis of 6U boards, yet are so interoperable that they connect with any USB4- or Ethernet-equipped system. Cooling the dense modules is accomplished via GMS patented RuggedCool<sup>™</sup> and RuggedCool<sup>2™</sup> hot spot cooling, floating heat plane, and multi-link 3-side cooling wedgelocks. Collectively, these cooling solutions provide over 100W+ of cooling per 3U OpenVPX slot.

#### Interoperable: Leverages Latest COTS USB4 and 100Gb Ethernet Standards

What makes the X9 SPIDER VPX boards unique in the industry is not one thing: it is everything. At the core, the boards use Intel's<sup>®</sup> latest 11<sup>th</sup>-generation Tiger Lake-H 8-core CPU with up to 64GB of ECC protected and upgradeable DDR4 DRAM. PCIe Gen 4 onboard fabrics move data to the backplane and the 100 Gb Ethernet "fat pipe" ports at 16GT/s with ultra-low latency. As a SOSA-aligned payload profile board, all mandatory I/O is present on the P0-P2 connectors, making the board backplane interoperable with other vendors and in spirit with the "DoD's Tri-Service memo" requiring a Modular Open Standards Approach (MOSA) to systems design.

It's hard to think of an electronics open standard more common than USB, and X9 SPIDER VPX-HS and -S use four plug-and-play sealed USB4 40Gbps Type-C ports with Power Delivery. These ports not only transmit and receive 40Gbps high-bandwidth off-board data via the front panel, but provide Power Delivery up to 100W+ that can power a distant sensor or entire embedded system, simplifying system architecture design and cables, while saving weight by alleviating a distant system's power supply. GMS provides custom rugged or sealed IP-rated cables (copper or fiber, with optional power) for out-of-the-chassis connections to 38999 mil-circular.

For highest speed and long-haul data movement, the X9 SPIDER VPX-HS is also equipped with dual 100Gbps Ethernet (fiber, 1km) ports. These dual "fat pipes" are an ideal way to create inter-chassis architectures, dual redundancy, or merely to increase by 10x (per port) the limited 10Gb Ethernet available on the OpenVPX backplane. 100Gb Ethernet is an unheard-of feature on 3U OpenVPX modules and X9 SPIDER VPX-HS has two.

## **RAID Storage, GPGPU and Add-in Functions**

Hard disk storage on 3U OpenVPX is uncommon, and X9 SPIDER takes it to a new level with two to four high density M.2 SSD sites: 2x on X9 SPIDER VPX-S and 4x on VPX-HS. Arranged via software RAID, the drives can realize RAID 0-50 on-board, saving a backplane slot elsewhere. As well, dual PCIe-Mini SAM<sup>™</sup> I/O sites support legacy I/O like MIL-STD-1553, ARINC-429, CANbus<sup>™</sup>, octal serial, GPS and more.

#### Tech Refresh via Backplane Upgrades, Plus Scalable Beyond the Box

Consisting of two (VPX-S) or three (VPX-HS) individual, pluggable circuit boards, X9 SPIDER provides an upgrade path for future OpenVPX systems. Once designed and qualified into a system, should requirements change, GMS can upgrade the X9 SPIDER VPX module by modifying the individual boards in the module, while keeping the backplane signal routing "fixed." This alleviates the highest cost of OpenVPX system upgrades: changing the backplane. With GMS X9 SPIDER VPX modules, customers can keep the backplane constant through multiple technology refresh cycles, essentially future-proofing one's system

design. For convenience, X9 SPIDER VPX-HS and VPX-S are powered from a single +12VDC backplane voltage, further simplifying system design changes.

As described, both X9 SPIDER VPX modules are well-equipped with system-level features that allow designers to replace many existing boards with just one GMS X9 SPIDER module set. But the sheer functional density of the modules, plus the sealed quad USB4 ports, allow the modules to work stand-alone, outside of an OpenVPX chassis. In fact, the entire X9 SPIDER architecture is designed to be used as a MOSA open standards module in SOSA-aligned OpenVPX form, or in alternate, interconnected, and upgradeable configurations connected via plug-and-play USB4 40Gbps ports. Consult GMS for additional details.

All in all, X9 SPIDER VPX-HS and X9 SPIDER VPX-S SOSA-aligned OpenVPX modules truly are "single board systems."

## **Designed in America**

GMS has proudly designed X9 SPIDER VPX-HS and X9 SPIDER VPX-S SOSA-aligned 3U OpenVPX modules in America—with the same design ethos as the company's successful MIL-SPEC conduction-cooled servers, VME and VPX single-board computers, and deployed conduction-cooled small form factor (SFF) chassis and systems.

• High-resolution images X9 SPIDER VPX-HS and VPX-S (www.gms4sbc.com/x9spider)

**Where**: Booth #7751, AUSA Annual Meeting and Expo, Washington, DC, October 11-13, 2021

For interviews at the show, contact Kelly Wanlass at 801-602-4723 or kelly@hcimarketing.com or GMS CTO Chris Ciufo at (360) 921-7556 or <u>cciufo@gms4sbc.com</u>

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## **About General Micro Systems:**

General Micro Systems (GMS) is the rugged server company. The company is known as the industry expert in highest-density, modular, compute-intensive, and rugged small form-factor embedded computing systems, servers, and switches. These powerful systems are ideal for demanding C5ISR defense, aerospace, medical, industrial, and energy exploration applications. GMS is an IEC, ISO, AS9100, NIST-800-171, and MIL-SPEC supplier with infrastructure and operations for long-life, spec-controlled, and configuration-managed programs.

Designed from the ground up to provide the highest performance and functionality in the harshest environments on the planet, the company's highly customizable products include GMS Rugged DNA<sup>™</sup> with patented RuggedCool<sup>™</sup> cooling technology. GMS is also the leader in deployable high-end Intel<sup>®</sup> processors and a proud Intel<sup>®</sup> partner since 1986. For more information, visit <u>www.gms4sbc.com</u>.

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